

Notice of Allowability

Application No.

10/634,917

Examiner

Monica Lewis

Applicant(s)

SAITO ET AL.

Art Unit

2822

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/13/05.
2. ☒ The allowed claim(s) is/are 1-4 and 9-11.
3. ☒ The drawings filed on 06 August 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
 Paper No./Mail Date 10/03;2/05;6&7/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit
 of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
 Paper No./Mail Date 04/05
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Mary Wilczewski
 Primary Examiner

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 Primary Examiner

EXAMINER'S AMENDMENT

1. Examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given via telephone by Eckhard H. Kuesters on April 13, 2005.

The application has been amended as follows:

Cancel claims 5-8, 12-23 and 25-30.

In the specification, page 1 line 2 before "POWER SEMICONDUCTOR", --A WIDEBAND GAP--has been inserted.

In the specification, page 1 line 2 after "POWER SEMICONDUCTOR DEVICE", -- HAVING A LOW ON-RESISTANCE AND HAVING A HIGH AVALANCHE CAPABILITY --has been inserted.

2. The following is an examiner's statement of reasons for allowance:

In regards to claim 1, the prior art fails to disclose the following: a) a third semiconductor layer between the control electrode and the second main electrode, the third semiconductor layer having a band gap of 3 eV or more.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2822


3. The following prior art made of record and not relied upon is considered pertinent to applicant's disclosure: a) Mizuta et al. (U.S. Patent No. 6,483,135) discloses a field effect transistor; b) Kawai et al. (U.S. Patent No. 6,064,082) discloses a heterojunction field effect transistor; c) Brasen et al. (U.S. Patent No. 5,442,205) discloses a heterostructure device; d) Ishii et al. (U.S. Publication No. 2001/0015437) discloses a field effect transistor; e) Hasegawa (U.S. Patent No. 5,399,886) discloses a heterojunction FET; and f) Ota et al. (U.S. Patent No. 5,905,277) discloses a field effect transistor.

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Monica Lewis whose telephone number is 571-272-1838. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-308-7722 for regular and after final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

ML

April 14, 2005



Mary Wilczewski
Primary Examiner